产品特征：

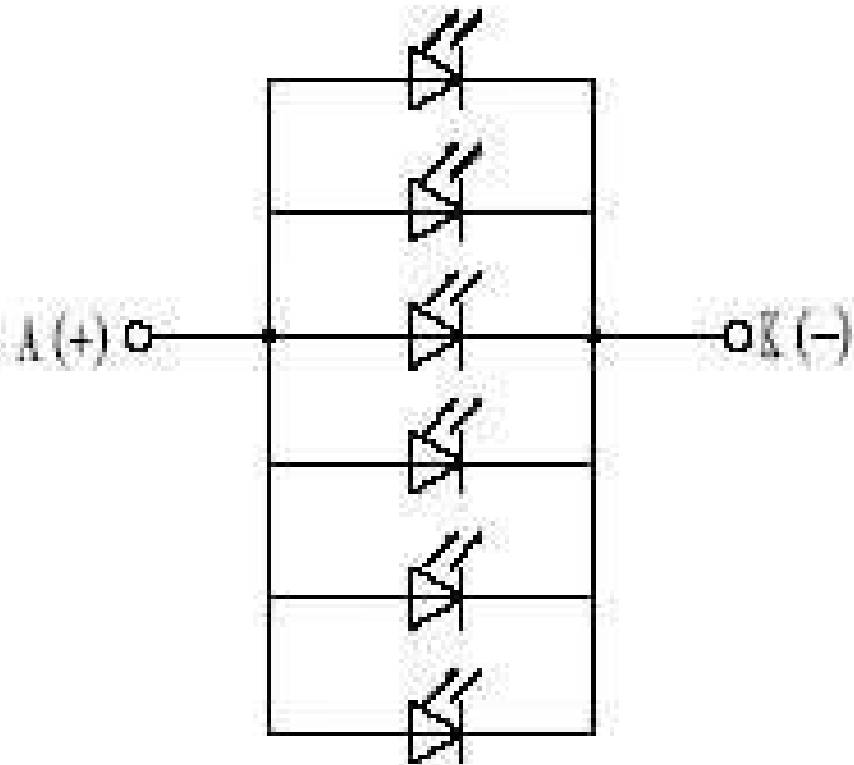| Display Mode	Transmissive
	a-TFT
Display Format	Graphic 320RGB*480 Dot-matrix
Input Data	3 线串口/4 线串口/8 bits /16bits parallel interface
Viewing Direction	Wide viewing(宽视角)
Drive	ILI9341

机械规格：

Item	Specifications	Unit
Dimensional outline	54.00(W)*77.40 (H)*3.45+/-0.15(T) (FPC not include)	mm
Resolution	240RGB*320	dots
LCD Active area	48.60(W)*64.80(H)	mm
Pixel size	0.2025(W)*0.2025(H)	mm

54.00 (LCM)
53.90 (TP)
48.60 (LCD A.A)
3.35

3.2"
240RGB x 320
宽视角

55°
XL   65° 65° XR
65°

YD

77.40 (LCM)
76.70 (TP)
64.80 (LCD A.A)

41.60
26.70
4.00

P0.5*(40-1)=19.50
20.50
5.00   44.00

3.45±0.15   2.40MAX

4-0.80

FPC+PI补强=0.30±0.03

55.34
63.00±0.1
3.00
4-R0.50

76.40±0.1
76.40±0.1
43.30
31.10

5.00±0.5
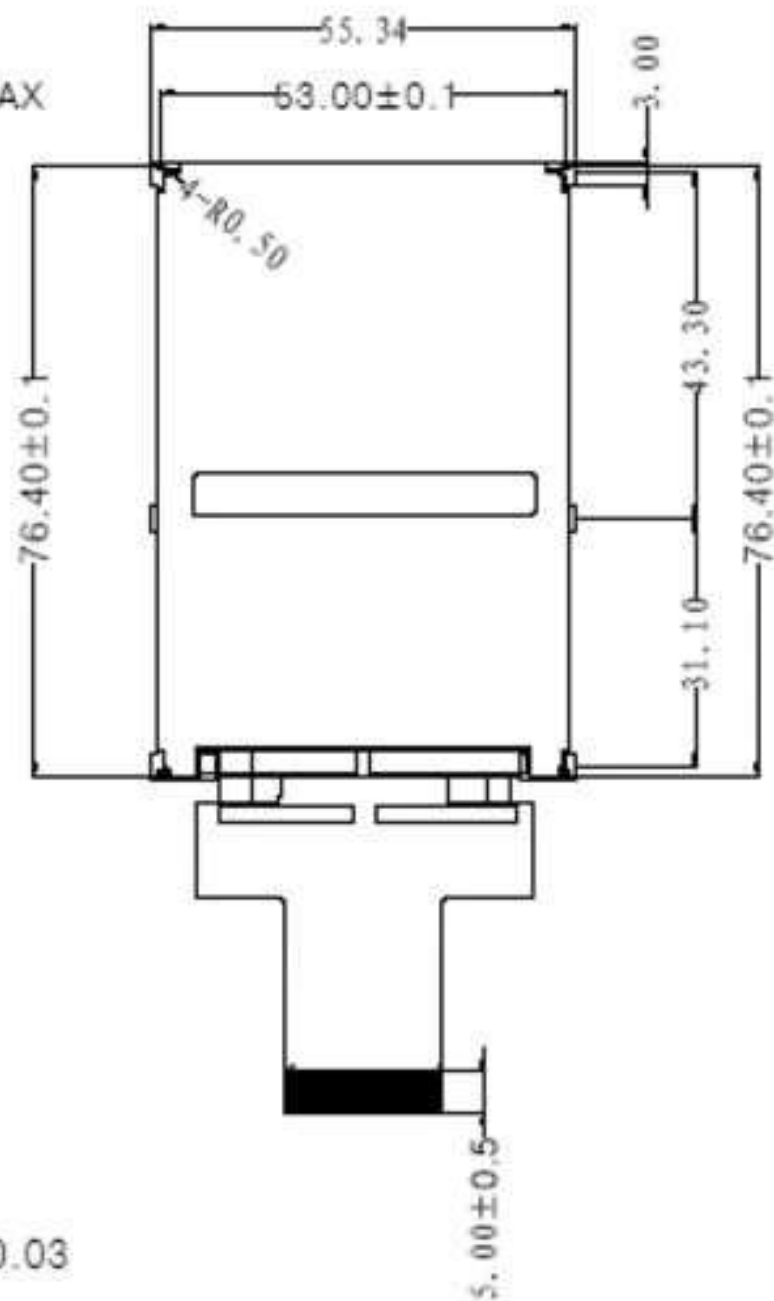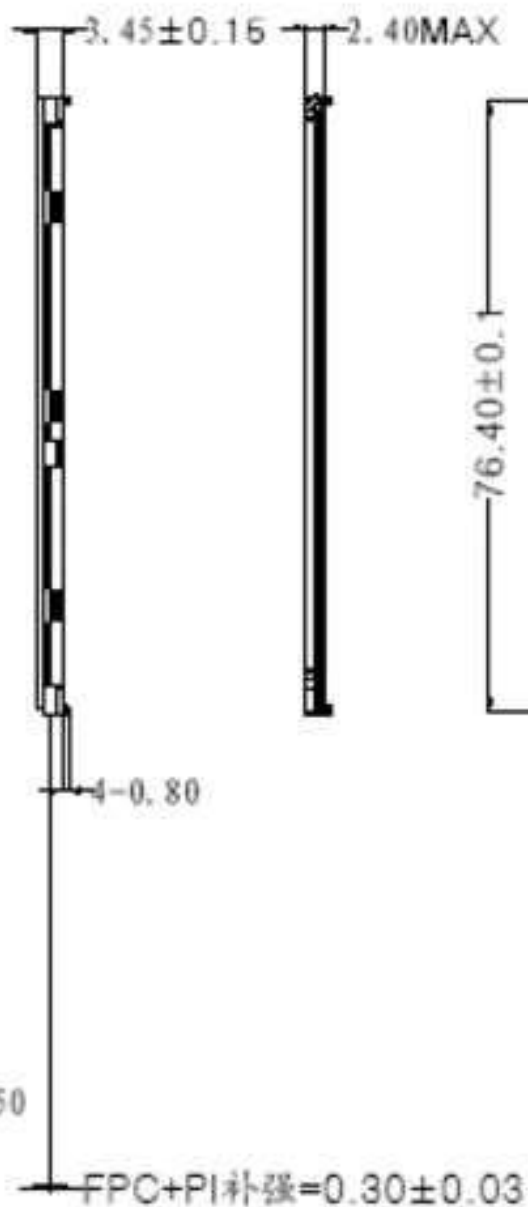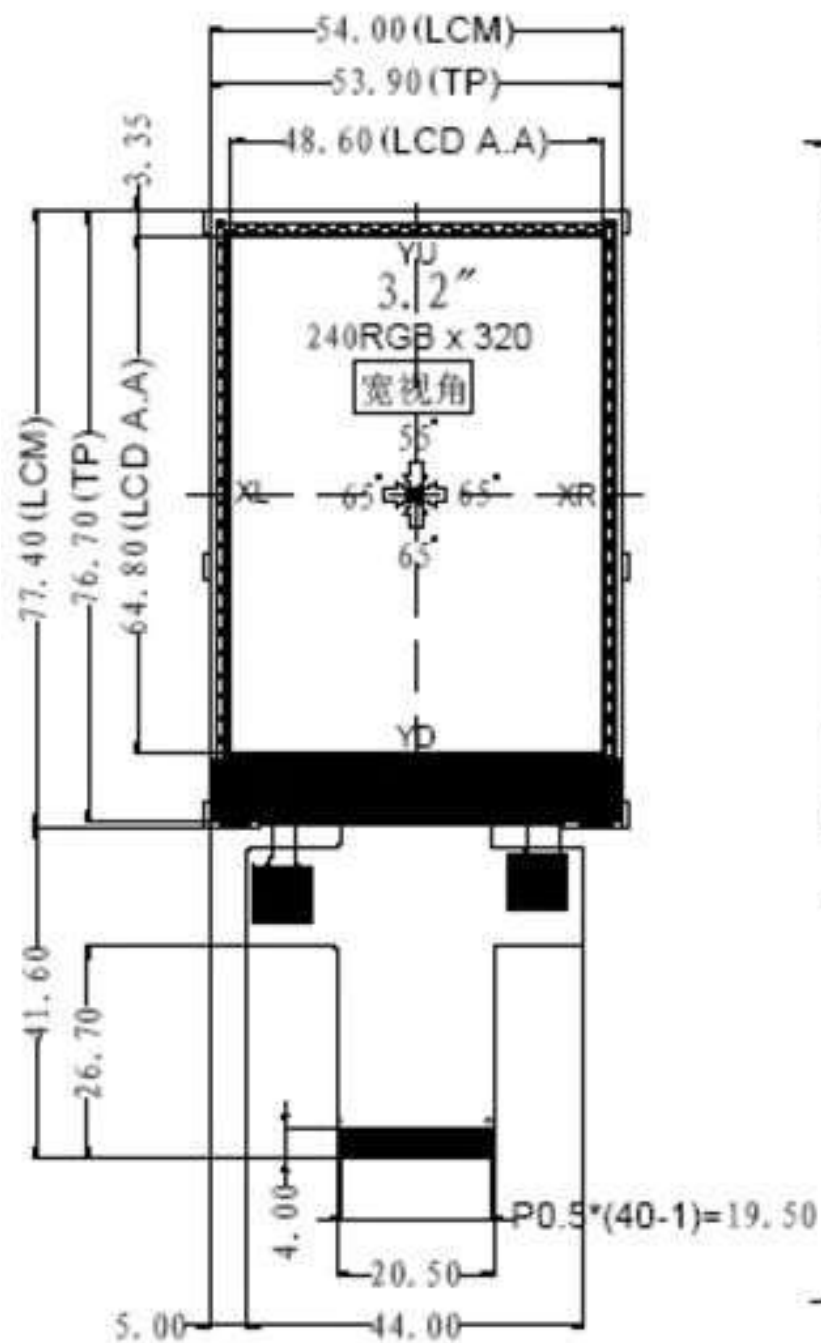
背光应用电路：

CIRCUIT DIAGRAM
(I=15mA * 6   V=3.0-3.4V)

PIN No.	Symbol	Description
1	X(L)	Touch panel control pin (触摸屏控制脚)
2	Y(U)	Touch panel control pin (触摸屏控制脚)
3	X(R)	Touch panel control pin (触摸屏控制脚)
4	Y(D)	Touch panel control pin (触摸屏控制脚)
5	GND	Ground (接地脚)
6	IOVCC	Power supply for LCM（2.8V-3.3V） (屏供电脚)
7	VCI	Power supply for LCM（2.8V-3.3V） (屏供电脚)
8	FMARK	Tearing effect output pin to synchronize MPU to frame writing, activated by S/W command. When this pin is not activated, this pin is low. If not used, open this pin. (帧同信号,不用时悬空)
9	CS/SPI CS	Chip select pin ("Low" enable) （屏驱动芯片片选脚，低电平有效）
10	RS/SPI SCL/SCK	This pin is used to select "Data or Command" in the parallel interface or serial data interface. (用于并口或者串口) Parallel(并口): When RS= '1', data is selected.(选择数据) When RS= '0', command is selected.(选择寄存器) Serial(串口): This pin is used serial interface clock in 3-wire 9-bit / 4-wire 8-bit serial data interface. (3线串口或者4线串口的时钟信号)

		If not used, this pin should be connected to IOVCC or GND. （不用时接 IOVCC 或者接地）
11	WR/A0（4 线）	Serves as a write signal and writes data at the rising edge. - 4-line system (D/CX): Serves as command or parameter select. *Fix to IOVCC level when not in use.* （并口的写控制脚或者 4 线串口的寄存器/数据选择，不用时接 IOVCC）
12	RD	Serves as a read signal and MCU read data at the rising edge. *Fix to IOVCC level when not in use.* （并口的读控制脚，不用时接 IOVCC）
13	SPI SDI/SDA	Serial input signal. The data is applied on the rising edge of the SCL signal. If not used, fix this pin at IOVCC or GND （串口数据输入信号，不用时接 IOVCC 或者接地）
14	SPI SDO	Serial output signal. The data is outputted on the falling edge of the SCL signal. If not used, open this pin （串口数据输出信号，不用时悬空）
15	RESET	LCM Reset pin Signal is active low. （屏复位脚，低电平复位）
16	GND	Ground （接地脚）
17-24	DB0-DB7	Data bus *Fix to GND level when not in use* （低 8 位数据线，不用时接地）
25-32	DB8-DB15	Data bus *Fix to GND level when not in use* （高 8 位数据线，不用时接地）
33	A	Anode of Backlight (3.0V-3.4V Typical:3.2V) （背光正极供电脚，电压范围:3.0-3.4V，典型值:3.2V）
34-36	K	Cathode of Backlight （背光负极供电脚）
37	GND	Ground （接地脚）

38	IM0	Select the MCU interface mode (接口选择)

IM2	IM1	IM0			
0	0	0	80 MCU 16-bit bus interface II	D[8:1]	D[17:10], D[8:1]
0	0	1	80 MCU 8-bit bus interface II	D[17:10]	D[17:10]
0	1	0	80 MCU 18-bit bus interface II	D[8:1]	D[17:0]
0	1	1	80 MCU 9-bit bus interface II	D[17:10]	D[17:9]
1	0	1	3-wire 9-bit data serial interface II	SDI: In SDO: Out	
1	1	0	4-wire 8-bit data serial interface II	SDI: In SDO: Out	

39	IM1

40	IM2

NOTE:

D[8:1]即低 8 位数据线 DB7-DB0D

D[17:10]即高 8 位数据线 DB15-DB8

关于供电说明：

IOVCC 和 **VCC** 连一起，用 **2.8V-3.3V** 供电；背光 **LED** 可以单独供电

（**3.0-3.4 V**），也可以和 **VCC** 共用一组电压（**A** 为正接 **VCC**，**K** 连一起作

为负接地）。